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SPECIFICATION

VXT430MBVS-03

☐ Preliminary Specification

☐ Final Specification

Customer:

Made By: **Checked By:** **Approved By:** **Quality:** **Date:** **Note:**	**Approved By:** **Date:** **Note:**

2. Revision Record

Date	Rev.No.	Page	Revision Items	Prepared
2023.3.10	**V00**		**First issued**	**SolonZhang**

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3. General Specifications

VXT430MBVS-03 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 4.3'' display area contains 480x(RGB)x800 pixels and can display up to 16.7M colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16.7M		1
Viewing Direction	ALL	O'Clock	
Operating temperature	-20~+70	℃	
Storage temperature	-30~+80	℃	
Module size	Refer to outline drawing	mm	2
Active Area(W×H)	56.16x93.60	mm	
Number of Dots	480(RGB)×800	dots	
Controller	ST7701SN	-	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Back light	9-LEDs	pcs	
Weight	---	g	
Interface	RGB888	-	

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.

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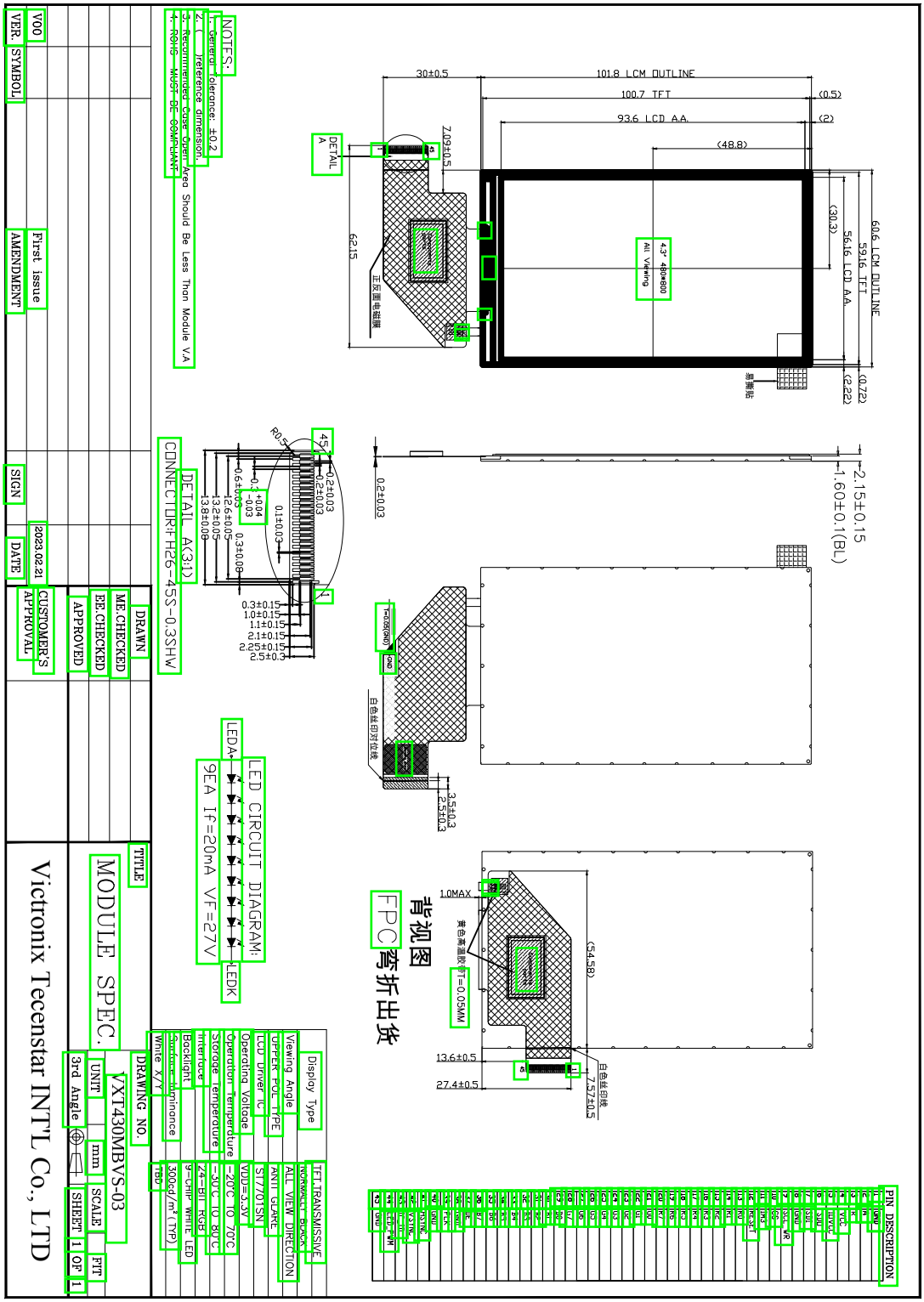
4. Outline Drawing



5. Absolute Maximum Ratings(Ta=25℃)

5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25℃)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VCC	-0.3	4.6	V	1,2
	IOVCC	-0.3	4.6	V	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. V_{CC} >V_{SS} must be maintained.

5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30℃	80℃	-20℃	70℃	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.
   The phenomenon is reversible.
3. Ta<=40℃:85%RH MAX.
   Ta>=40℃:Absolute humidity must be lower than the humidity of 85%RH at 40℃.

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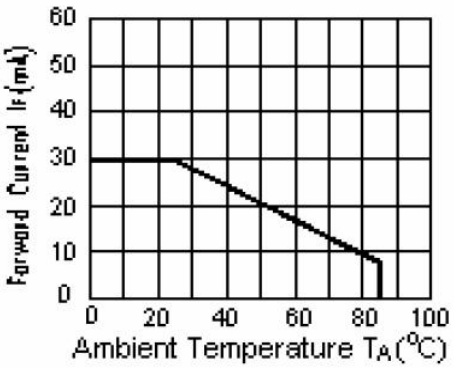
6. Electrical Specifications and Instruction Code

6.1 Electrical characteristics(Vss=0V ,Ta=25℃)

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Note
Power supply		VCC	Ta=25℃	2.5	3.3	3.6	V	
Power supply		IOVCC	Ta=25℃	1.65	1.8	3.3	V	
Input voltage	'H'	V_{IH}	--	0.7IOVCC	-	IOVCC	V	
	'L'	V_{IL}	--	0	-	0.3IOVCC	V	

6.2 LED backlight specification(VSS=0V ,Ta=25℃)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	VF	If=20mA	-	27	-	V	
Uniformity	ΔBp	If=20mA	80	-	-	%	
Life Time	time	If=20mA	20K	-	-	hours	1



Note 1: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25℃

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6.3 Interface signals

Pin No.	Symbol	I/O	Function
1	GND	P	Ground.
2	A	P	LED backlight(anode).
3	K	P	LED backlight(Cathode).
4	VCC	P	Power supply(Digital +3.3V)
5	IOVCC	P	Power supply(Digital +1.8V)
6	SDO	O	Serial data output pin used for the SPI Interface.
7	SDI	I	serial Input/output data bus.
8	GND	P	Ground.
9	SCL-WR	I	serial clock signal.
10	CS	I	chip select signal.
11	IM3	-	NO connect.
12	RESET	I	Global reset signal input pin.
13-20	R0-R7	I	Red data bus.
21-28	G0-G7	I	Green data bus.
29-36	B0-B7	I	Blue data bus.
37	DE	I	Data enable signal for RGB interface operation.
38	GND	P	Ground.
39	PLK	I	Dot clock signal for RGB interface operation.
40	GND	P	Ground.
41	HSYNC	I	Horizontal synchronous signal.
42	VSYNC	I	Vertical synchronous signal.
43	IC-ID	O	ID Pin.
44	LED-PWM	O	The PWM frequency output for LCD driver control.
45	GND	P	Ground.

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6.4 AC Characteristics

6.4.1 Serial Interface Characteristics (3-line serial)

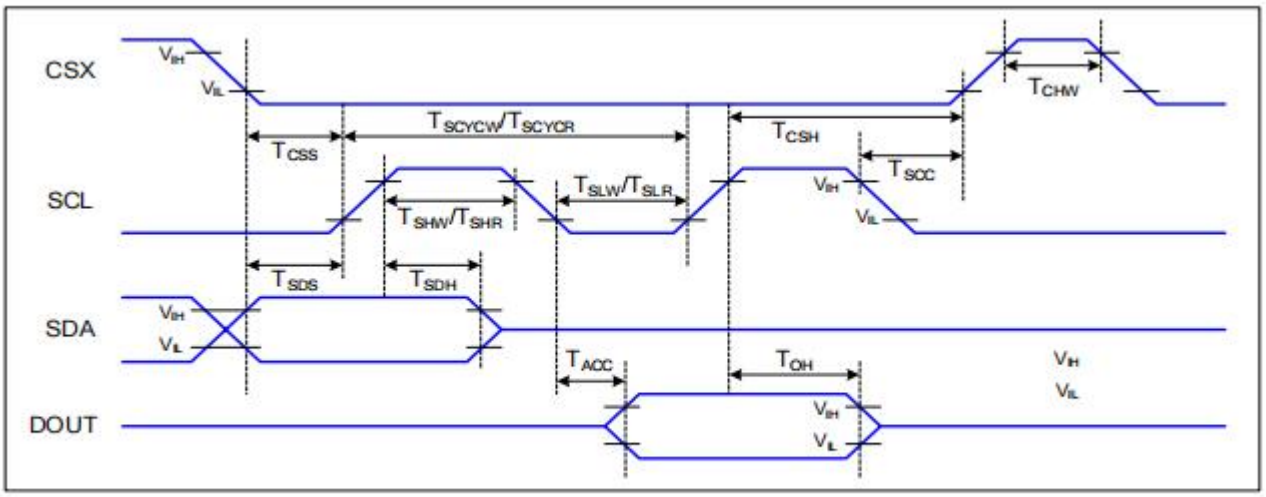


Figure 1 3-line serial Interface Timing Characteristics

Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	60		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	
	T_{SHW}	SCL "H" pulse width (Write)	15		ns	
	T_{SLW}	SCL "L" pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T_{SDS}	Data setup time	10		ns	
	T_{SDH}	Data hold time	10		ns	
SDO (DOUT)	T_{ACC}	Access time	20	50	ns	Max: CL=30pF
	T_{OH}	Output disable time	50	50	ns	Min: CL=8pF

Table 4 3-line serial Interface Characteristics

6.4.2 RGB Interface Characteristics

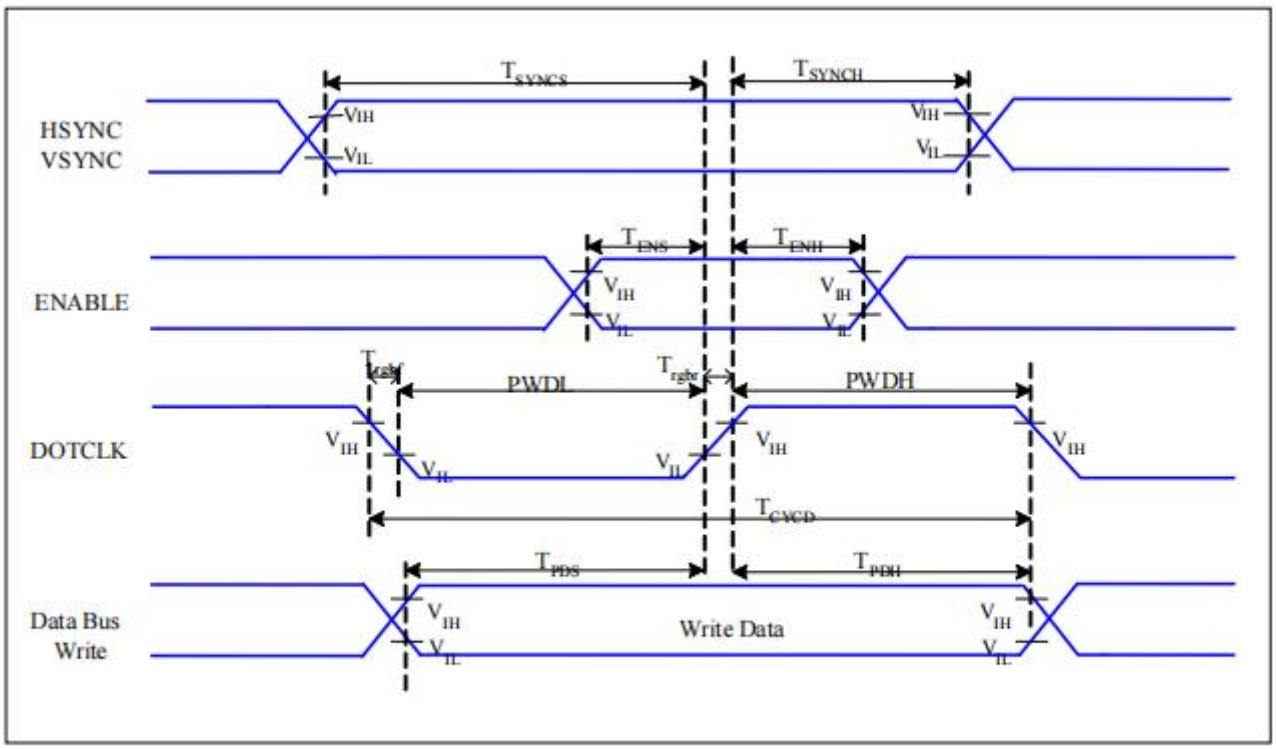


Figure 3 RGB Interface Timing Characteristics

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCS}	VSYNC, HSYNC Setup Time	5	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	5	-	ns	
	T_{ENH}	Enable Hold Time	5	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	15	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	15	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	33	-	ns	
	Trghr, Trghf	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	5	-	ns	
	T_{PDH}	PD Data Hold Time	5	-	ns	

Table 6 18/16 Bits RGB Interface Timing Characteristics

6.5 Reset Input Timing

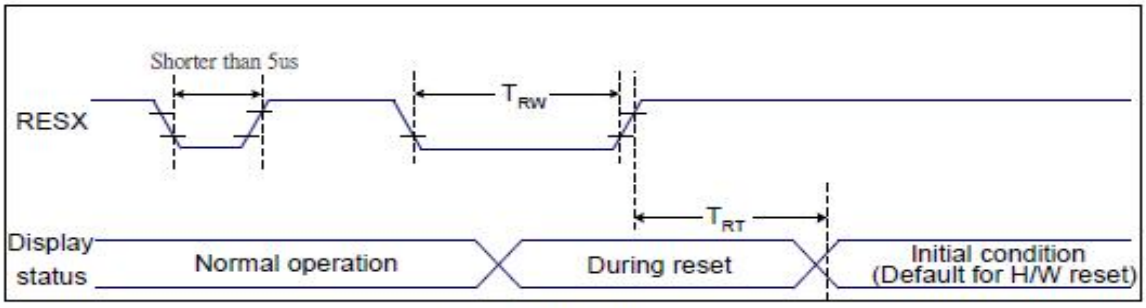


Figure 9 Reset Timing

VDDI=1.8,VDD=2.8, AGND=DGND=0V, Ta=25 ℃

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120(Note 1, 6, 7)	ms

Table 9 Reset Timing

Notes:

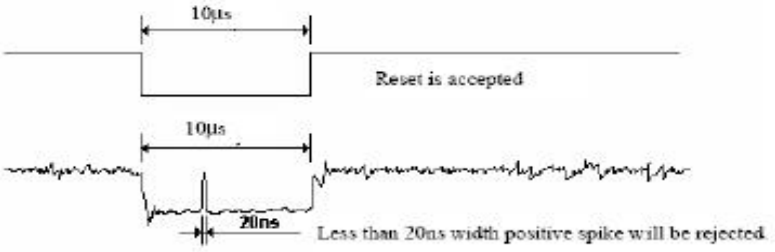
1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.

4. Spike Rejection also applies during a valid reset pulse as shown below:



5. When Reset applied during Sleep In Mode.

6. When Reset applied during Sleep Out Mode.

7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

7. Optical Characteristics

Item	Symbol		Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp		θ=0° Φ=0°	-	300	-	Cd/m^2	1
Uniformity	⊿Bp			80	-	-	%	1,2
Viewing Angle	3:00		Cr≥10	-	85	-	Deg	3
	6:00			-	85	-		
	9:00			-	85	-		
	12:00			-	85	-		
Contrast Ratio	Cr		θ=0° Φ=0°	700	1000		-	4
Response Time	T_r			-	25	35	ms	5
	T_f						ms	
Color of CIE Coordinate	W	x	θ=0° Φ=0°	Typ-0.05	0.298	Typ+0.05	-	1,6
		y			0.337		-	
	R	x			0.659		-	
		y			0.323		-	
	G	x			0.275		-	
		y			0.580		-	
	B	x			0.160		-	
		y			0.155		-	
NTSC Ratio	S			55	60	-	%	

Note: The parameter is slightly changed by temperature, driving voltage and materiel

Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment BM-7 (Φ5mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature: Ta=25℃.
- Adjust operating voltage to get optimum contrast at the center of the display.

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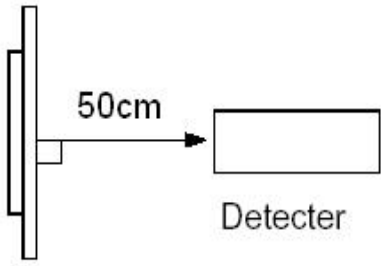
Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

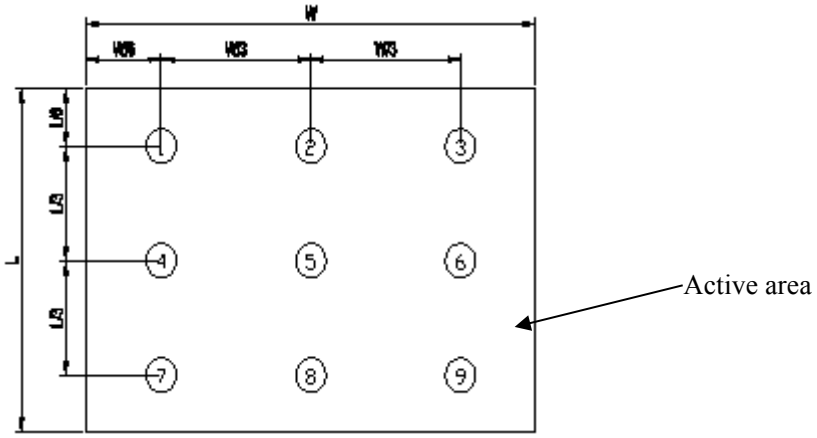


Note 2: The luminance uniformity is calculated by using following formula.

⊿Bp = Bp (Min.) / Bp (Max.)×100 (%)

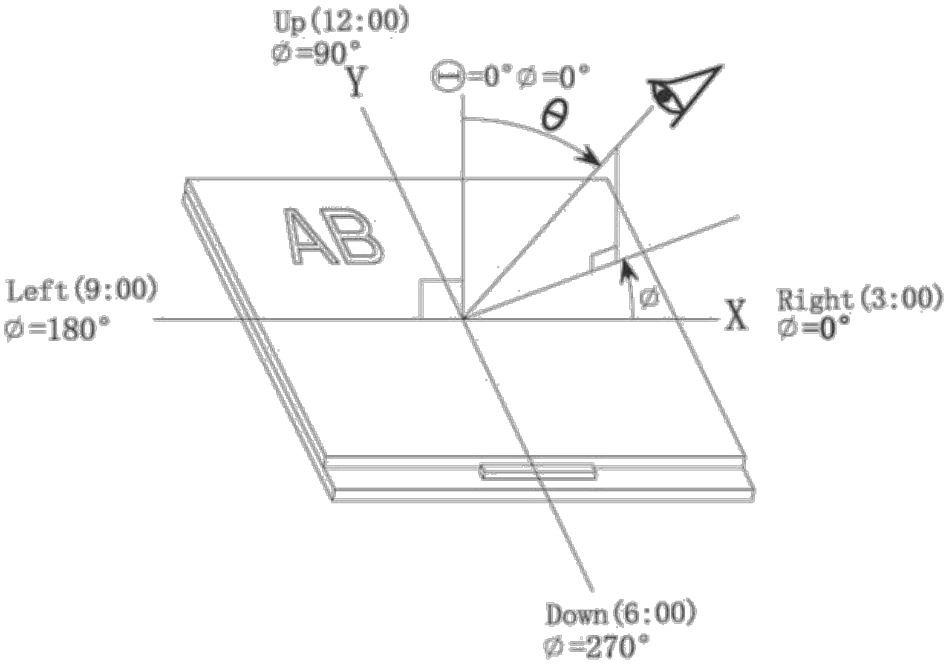
Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots.

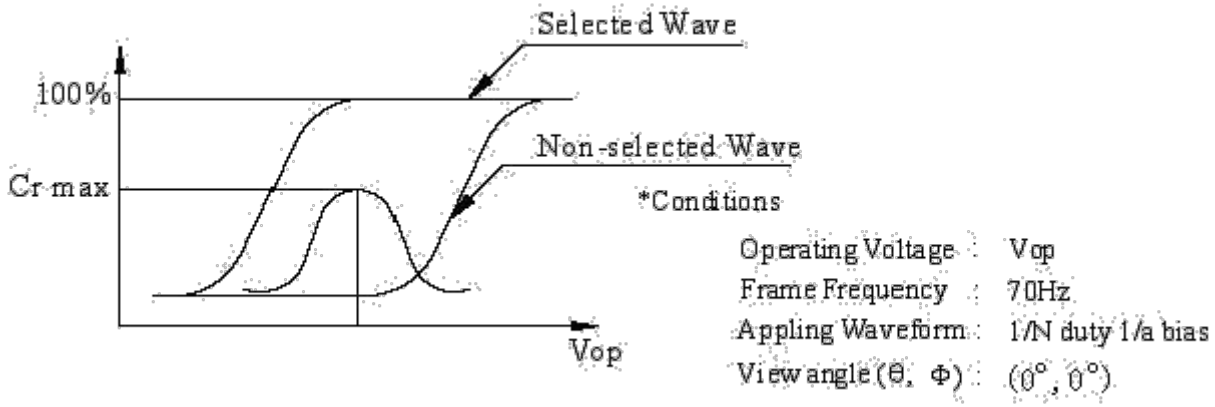


Note 3: The definition of viewing angle:

Refer to the graph below marked by θ and Φ



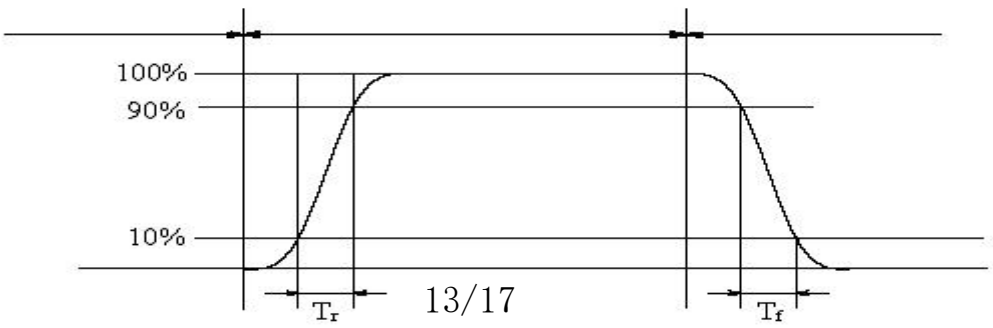
Note 4: Definition of contrast ratio.(Test LCD using DMS501)



$$Contrast\ ratio(Cr) = \frac{Brightness\ of\ \ selected\ dots}{Brightness\ of\ \ non-selected\ dots}$$

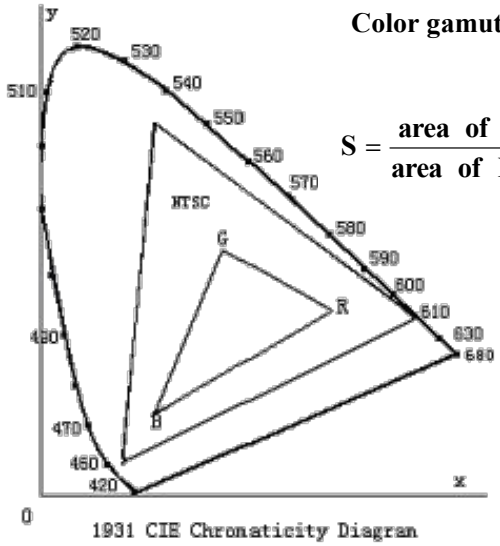
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



The definition of response time`

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

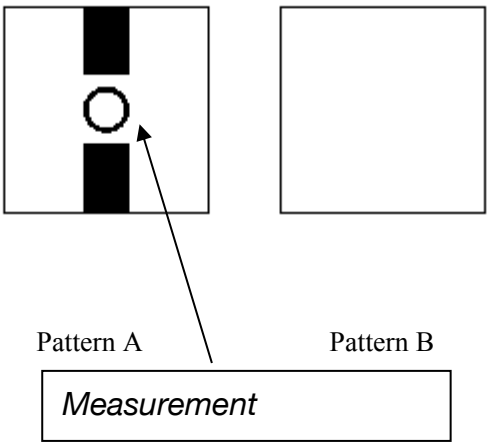


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

Cross talk ratio(%)=|pattern A Brightness-pattern B Brightness|/pattern A Brightness*100



Electric volume value=3F+/-3Hex

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8. Reliability Test Items and Criteria

Test Item	Test condition	Remark
High Temperature Storage	Ta = 80℃ 96hrs	Note1,Note4
Low Temperature Storage	Ta = -30℃ 96hrs	Note1,Note4
High Temperature Operation	Ts = 70℃ 96hrs	Note2,Note4
Low Temperature Operation	Ta = -20℃ 96hrs	Note1,Note4
Operation at High Temperature/Humidity	+60℃, 90%RH 96hrs	Note4
Thermal Shock	-20℃/30 min ~ +70℃/30 min for a total 10 cycles, Start with cold temperature and end with high temperature.	Note4
Package Drop Test	Height:60cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	±2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.

Note 2: Ts is the temperature of panel's surface.

Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature

9. Precautions for Use of LCD Modules

9.1 Handling Precautions

9.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping

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it from a high place, etc.

9.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

— Isopropyl alcohol — Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

— Water — Ketone — Aromatic solvents

9.1.6 Do not attempt to disassemble the LCD Module.

9.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

a. Be sure to ground the body when handling the LCD Modules.

b. Tools required for assembly, such as soldering irons, must be properly ground.

c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.2 Storage precautions

9.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.2.2 The LCD modules should be stored under the storage temperature range. If the LCD

modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ~ 40℃

Relatively humidity: ≤80%

9.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

- END

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